

PRIOR ART

FIG 1

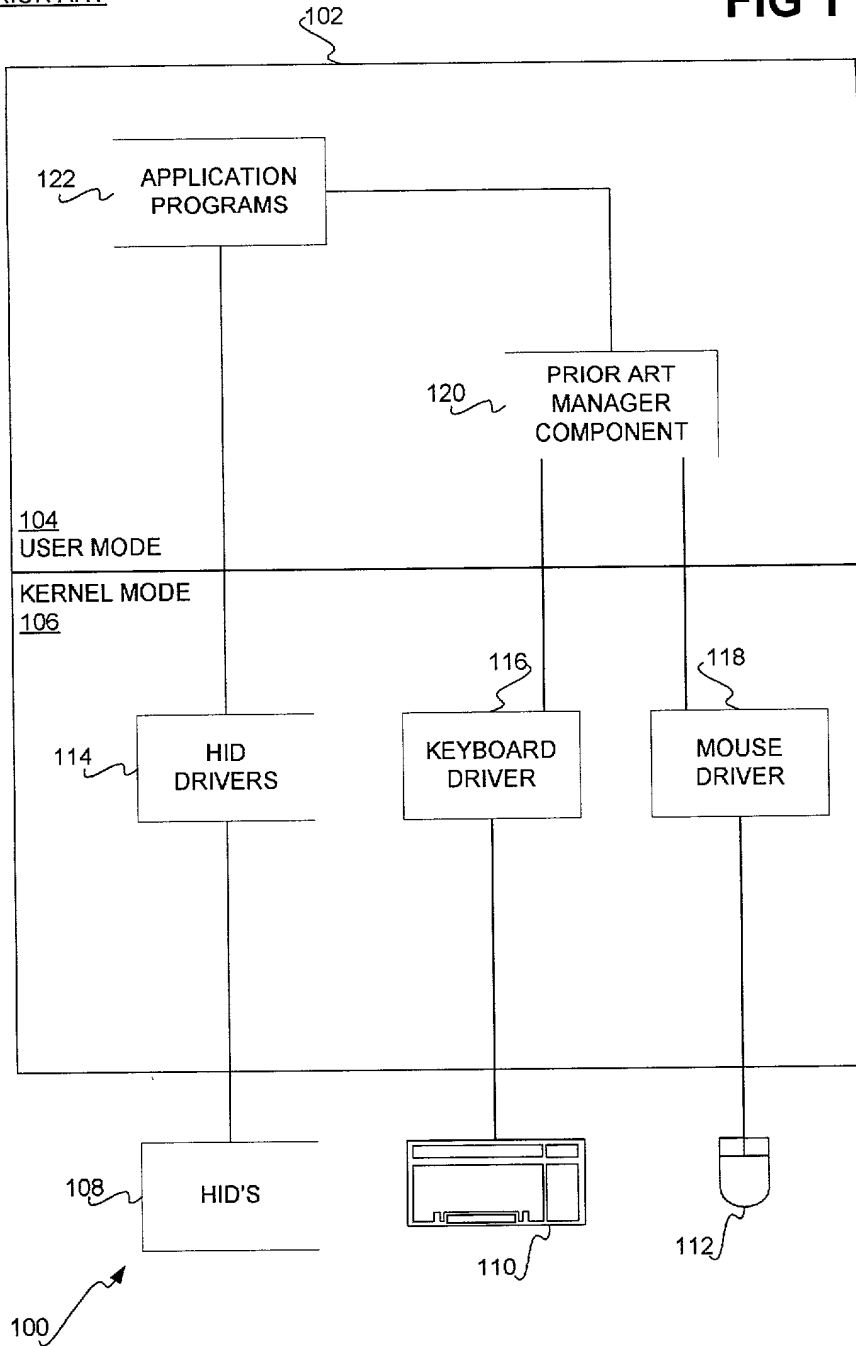
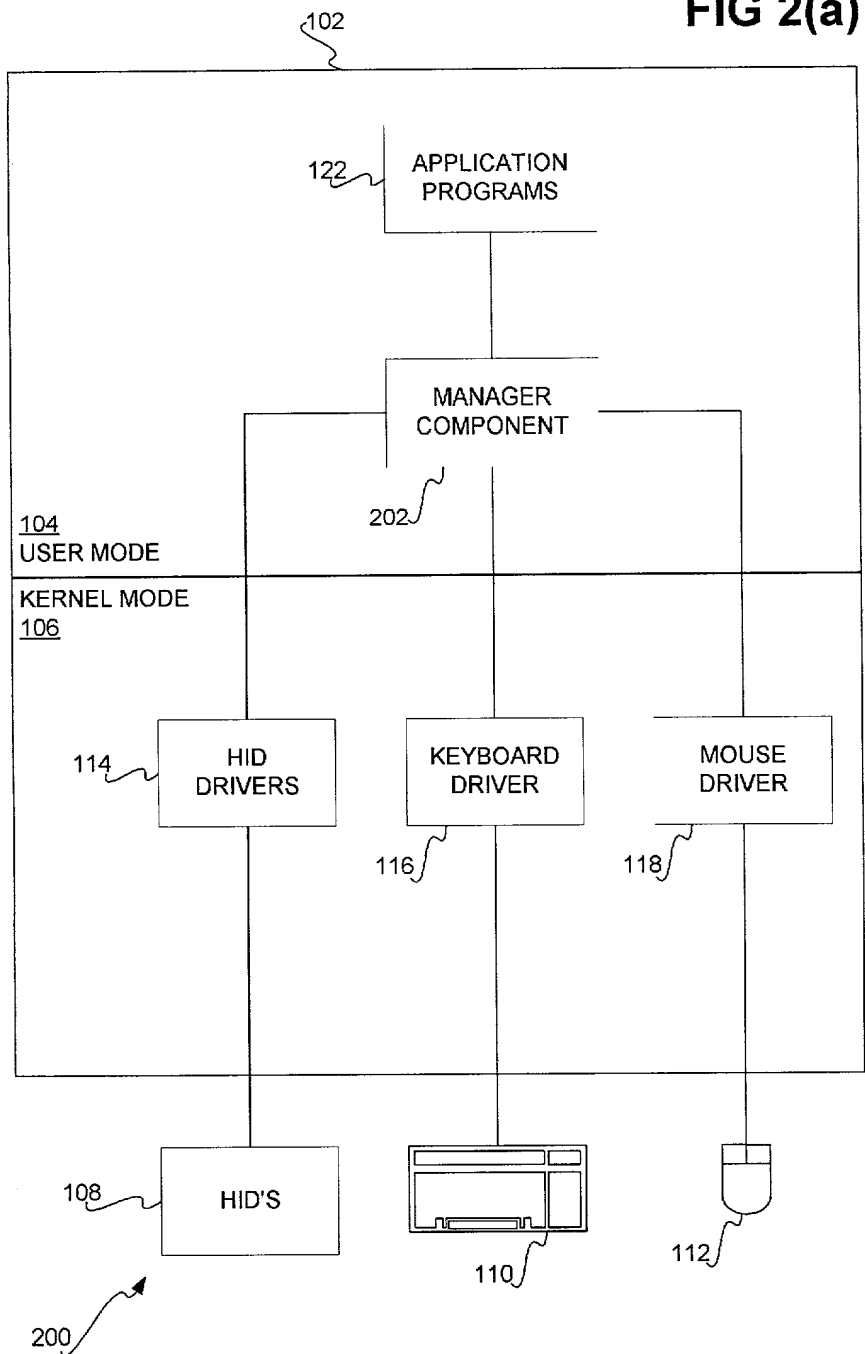


FIG 2(a)



REGISTRATION

FIG 2(b)

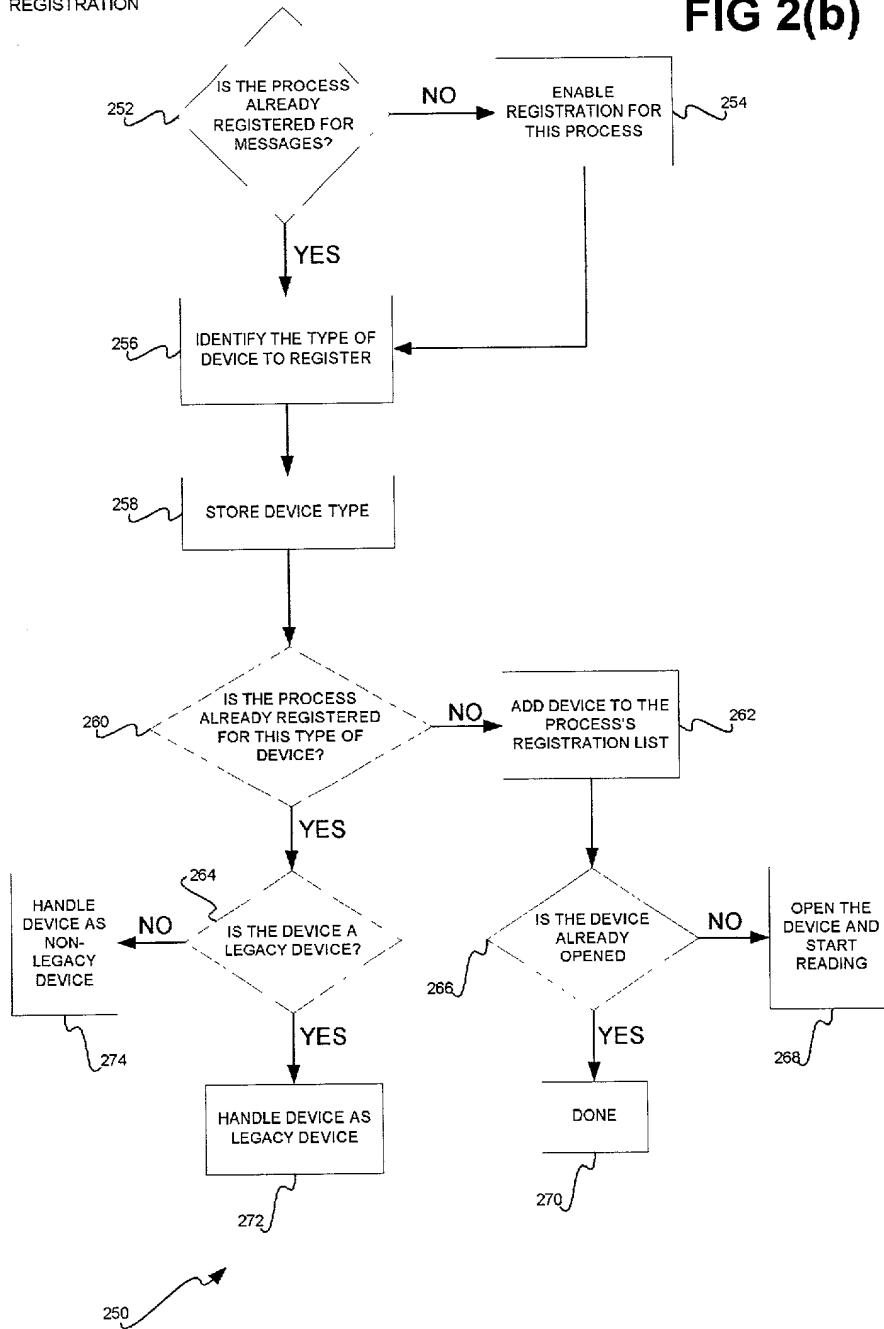


FIG 2(c)

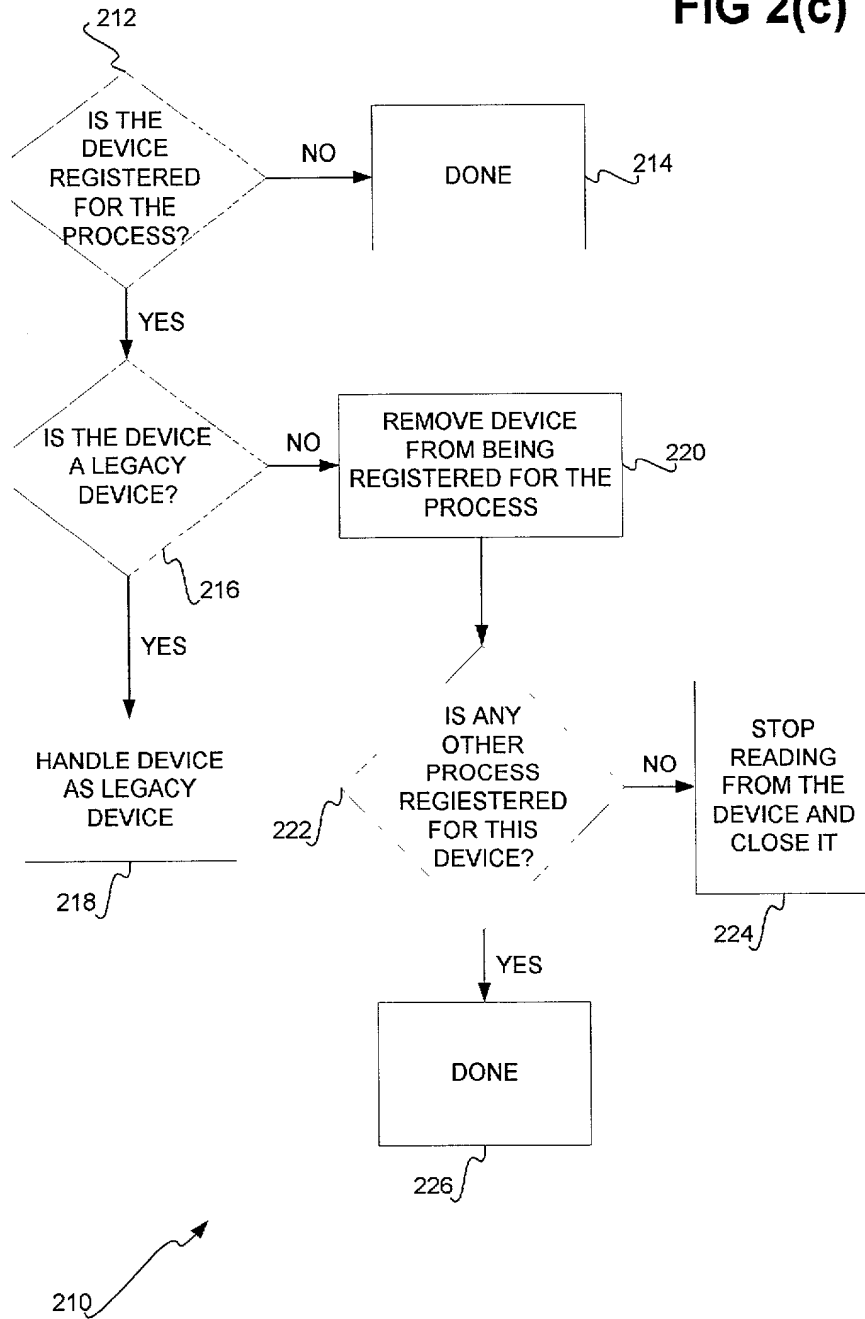
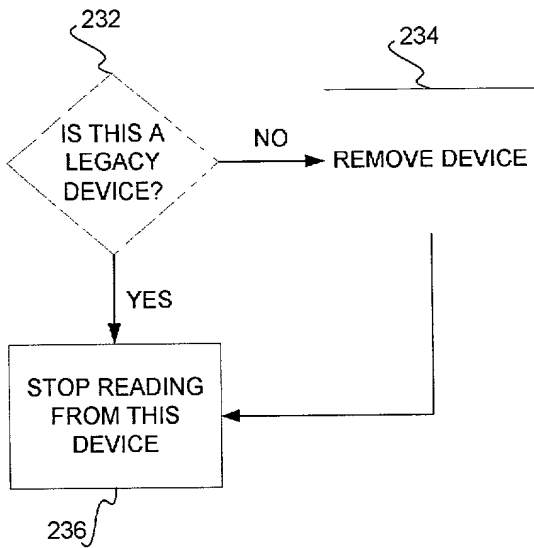


FIG 2(d)



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DEVICE ADDITION

FIG 2(e)

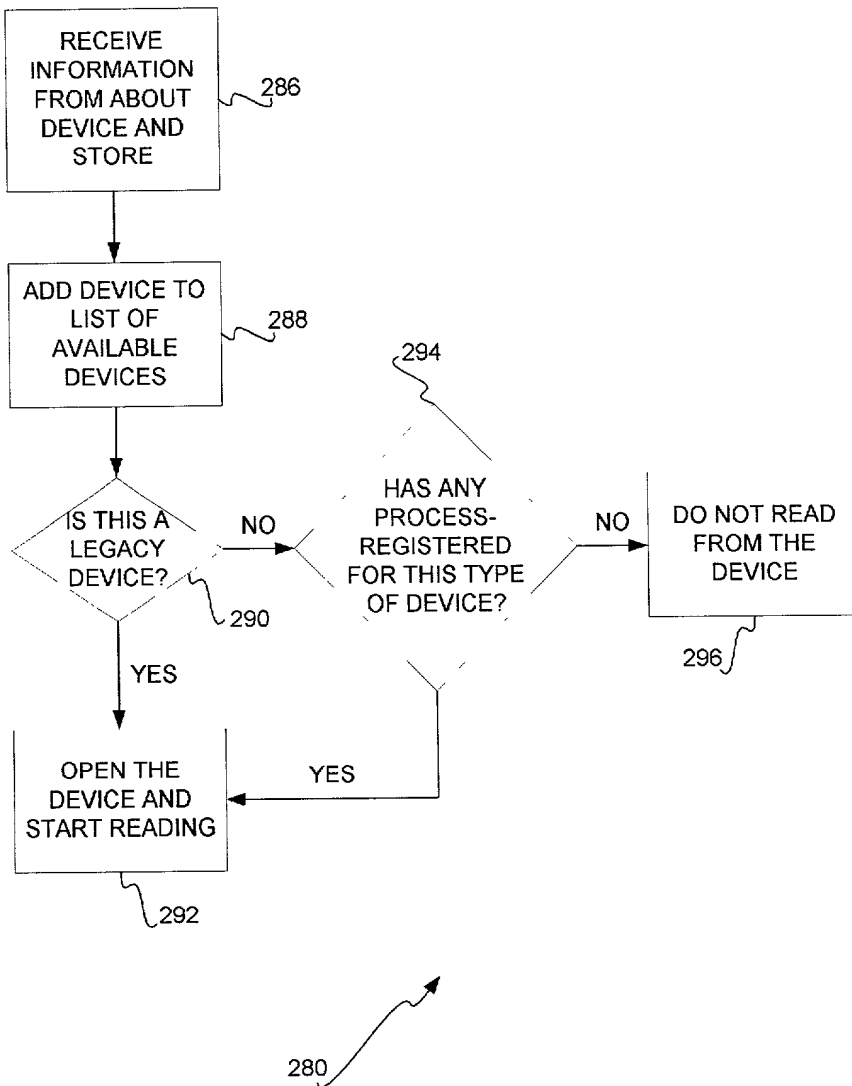


FIG 3

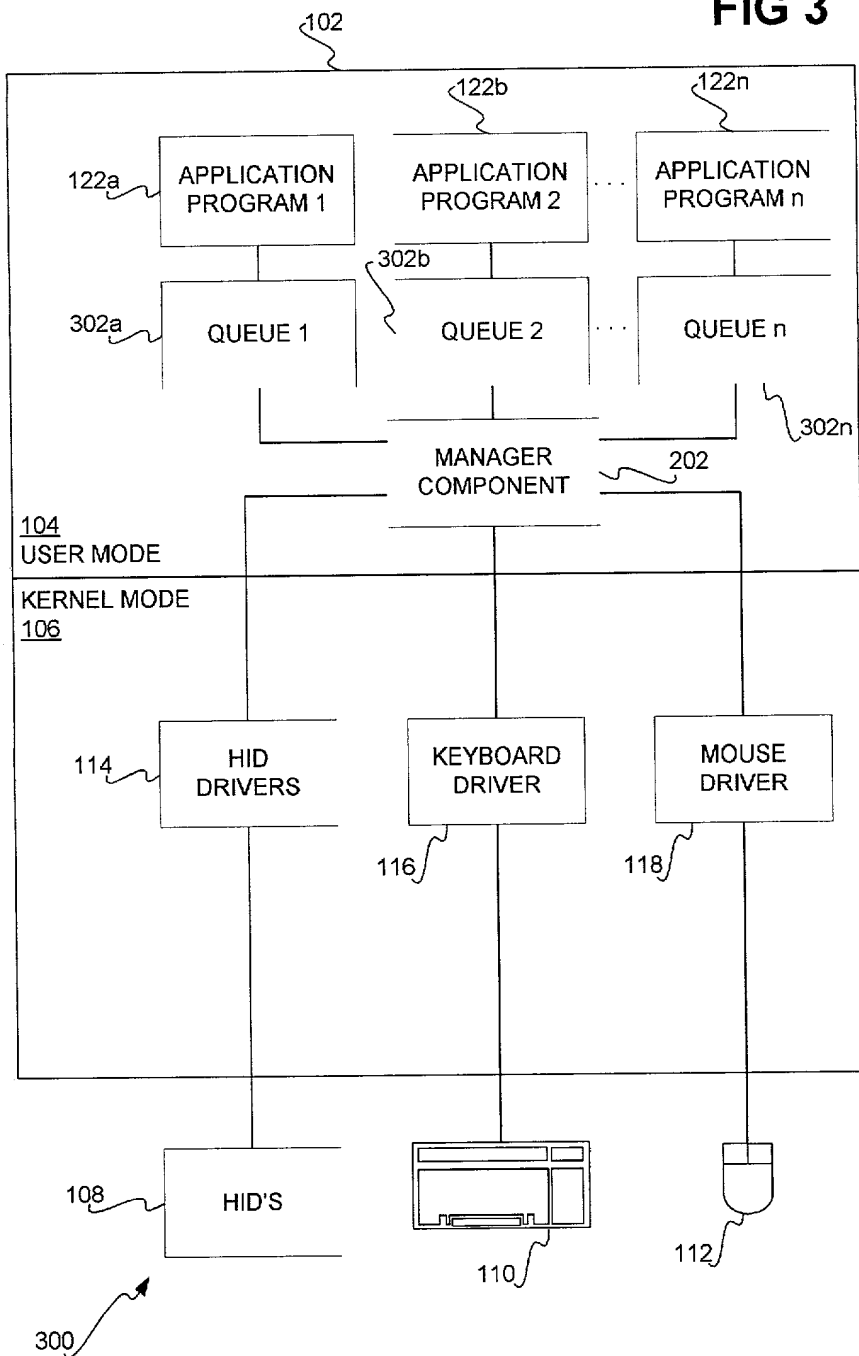
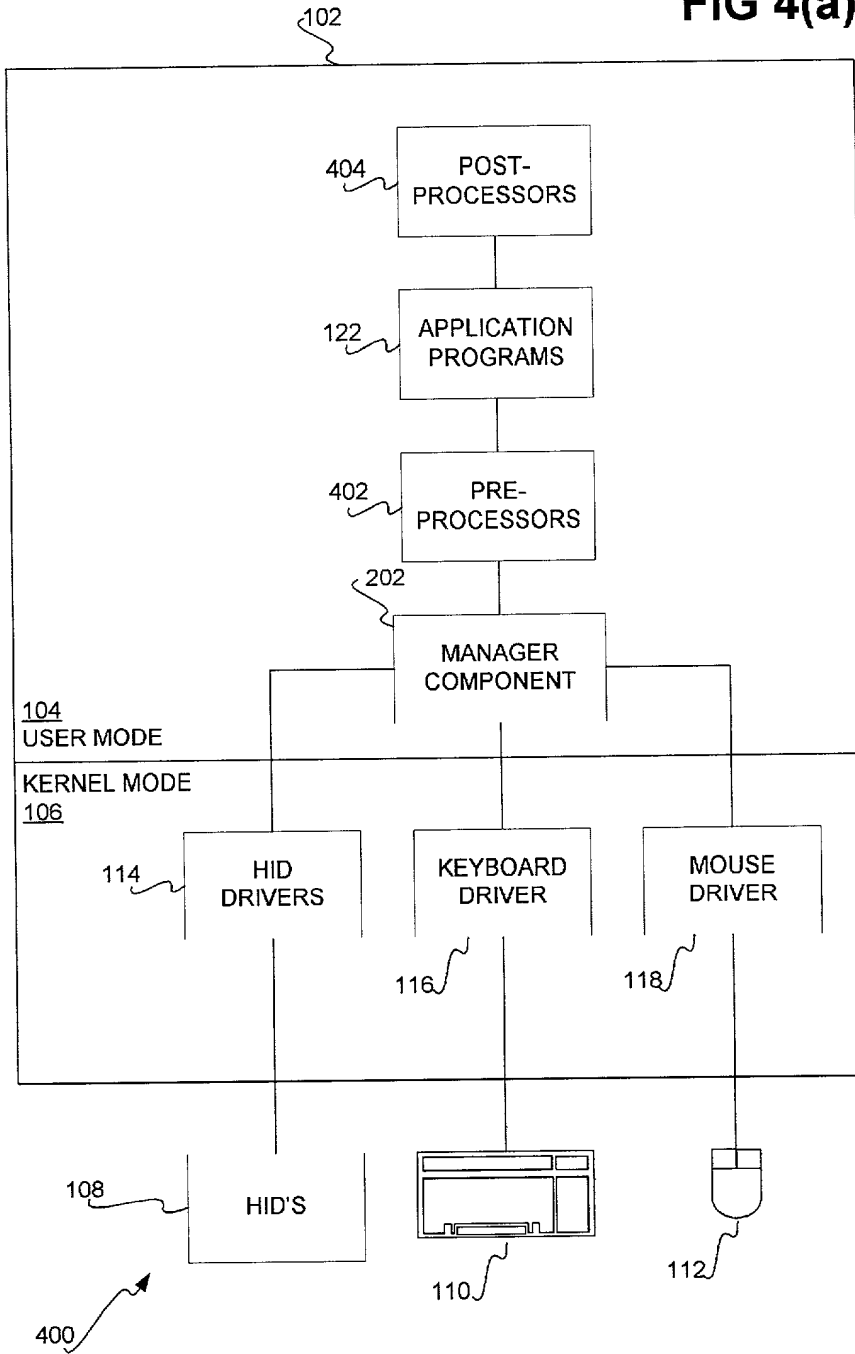


FIG 4(a)



MESSAGE PROCESSING
WITH PRE- AND POST-
PROCESSORS

FIG 4(b)

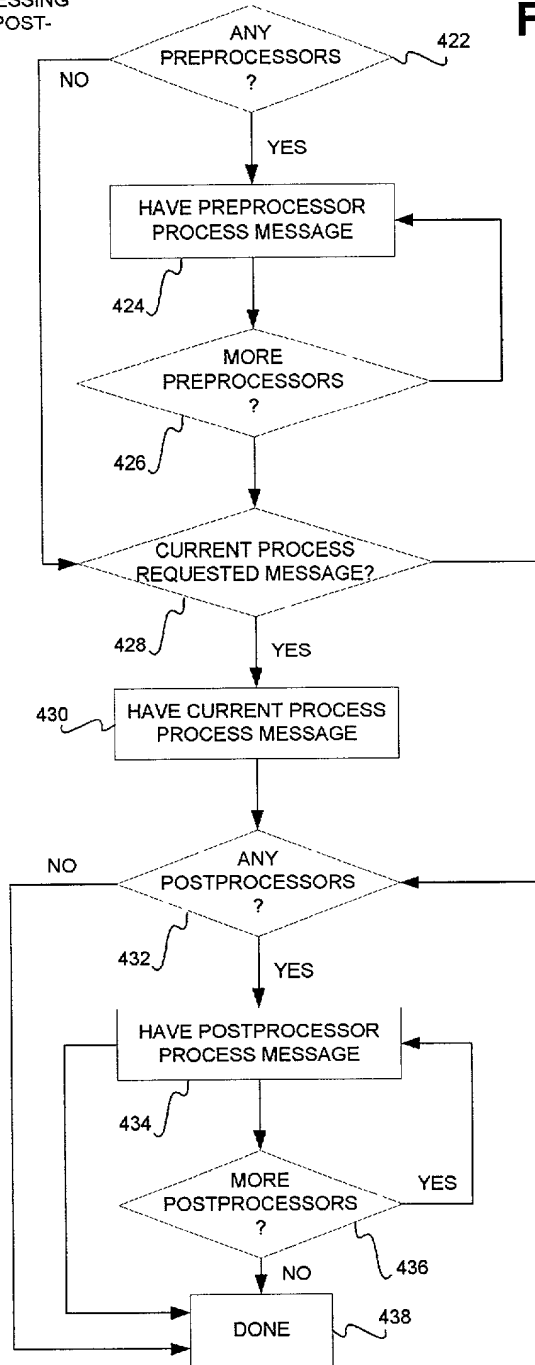


FIG 5

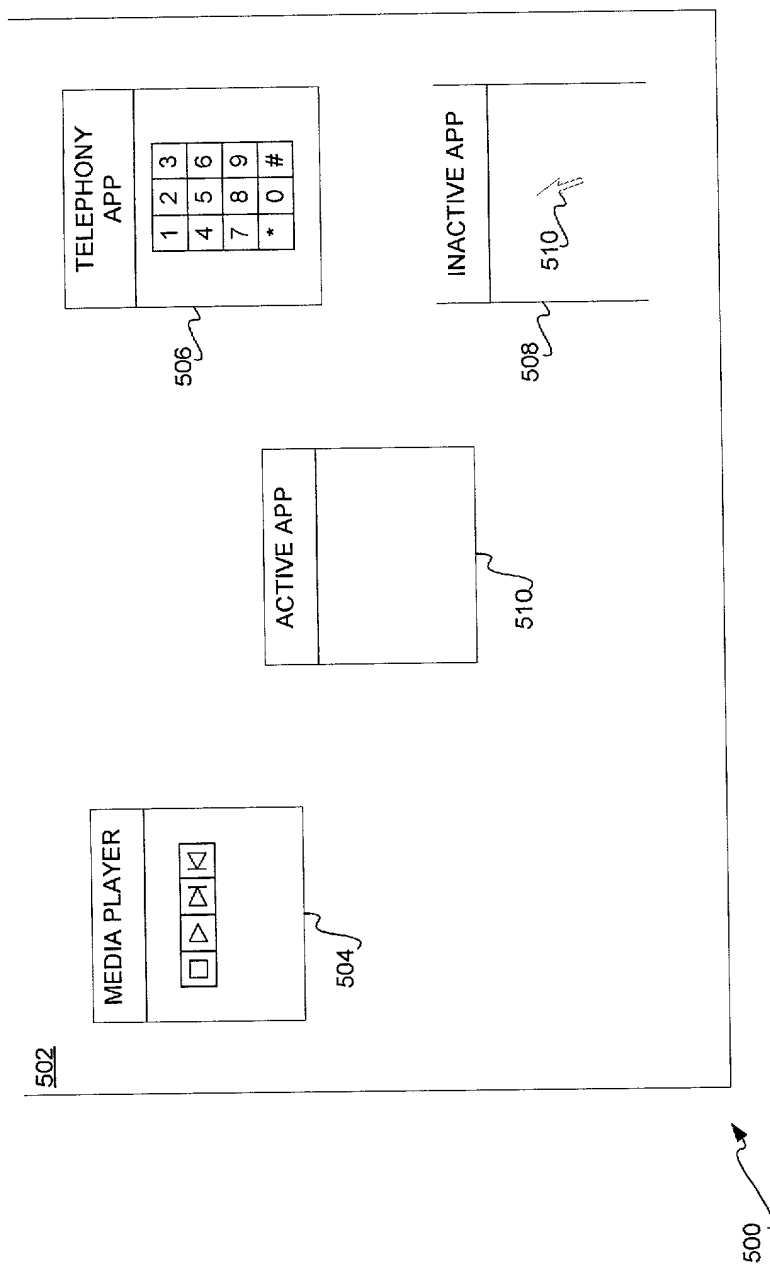


FIG 6

